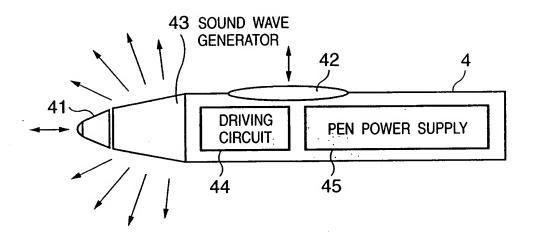
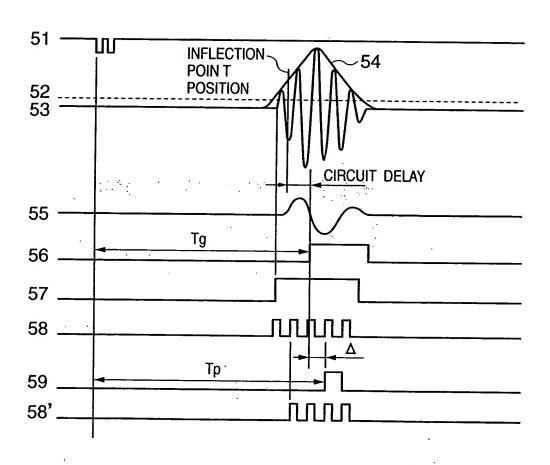


FIG. 2





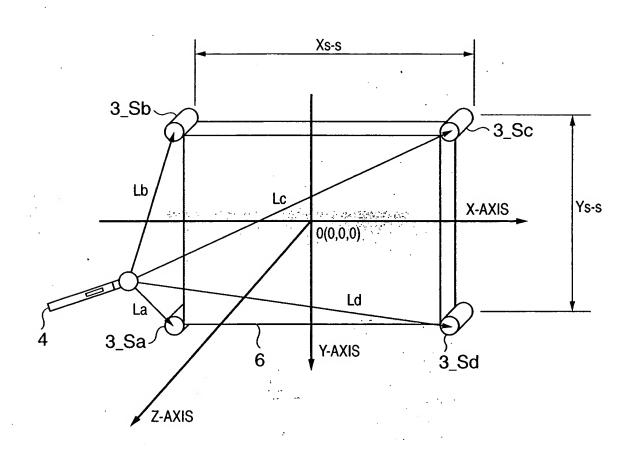
4.

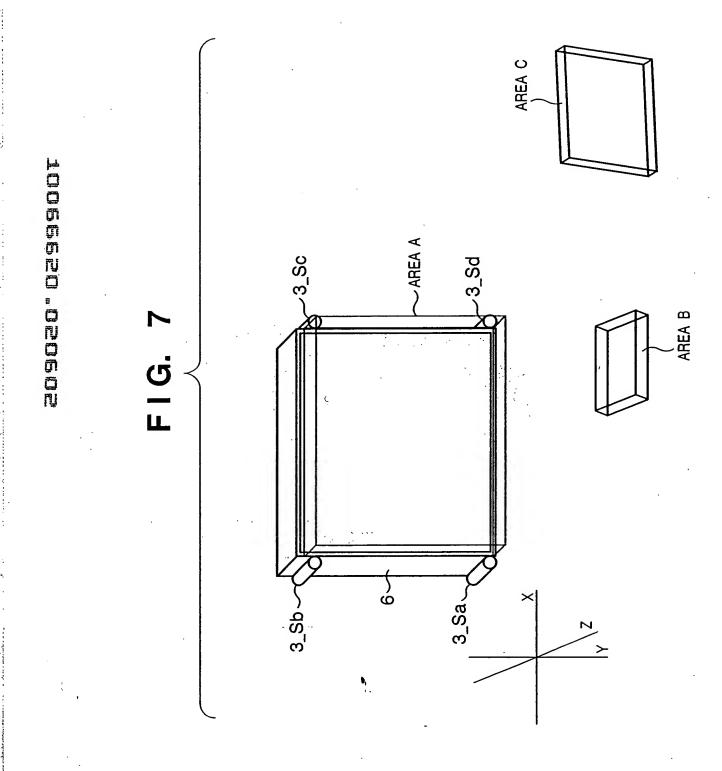
ARITHMETIC CONTROL CIRCUIT 98 Tg SIGNAL GENERATING CIRCUIT Tp SIGNAL DETECTION CIRCUIT ENVELOPE CARDINAL POINT DETECTION CIRCUIT 82 85 GATE SIGNAL GENERATING CIRCUIT FIG. 84 ENVELOPE DETECTION CIRCUIT BANDPASS FILTER | PREAMPLIFYING | CIRCUIT 80 5/2

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,15_d LATCH CIRCUIT Tp_d SIGNAL WAVEFORM DETECTION CIRCUIT LATCH CIRCUIT 15_c DETECTION SIGNAL INPUT PORT Jp_c LATCH CIRCUIT 15_b TIMER Tp_b LATCH CIRCUIT 15_a F G Тр_а DETERMINATION CIRCUIT (EXTERNAL CIRCUIT) 7 MICROCOMPUTER START SIGNAL DETECTION CIRCUIT I/O PORT .

FIG. 6





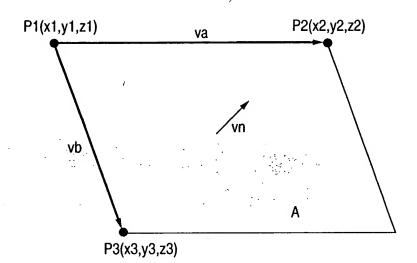


FIG. 9

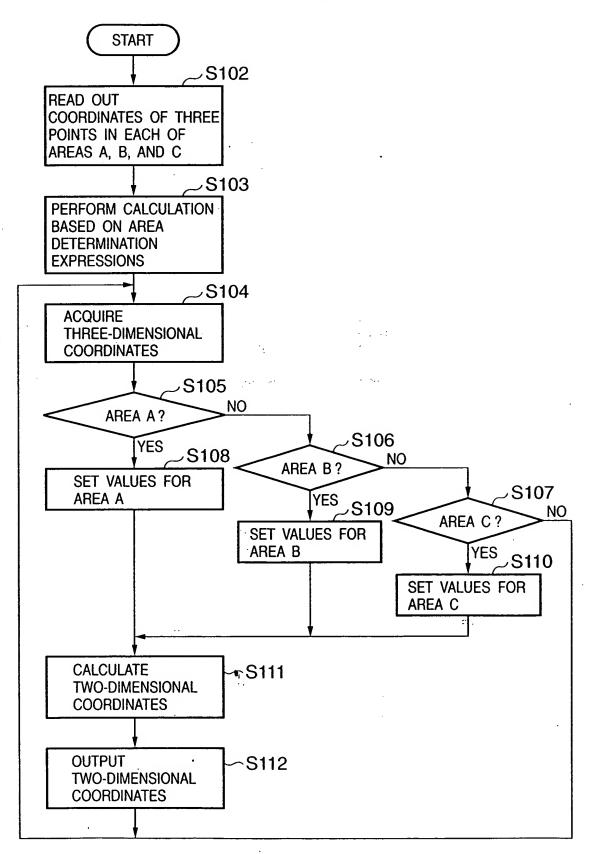
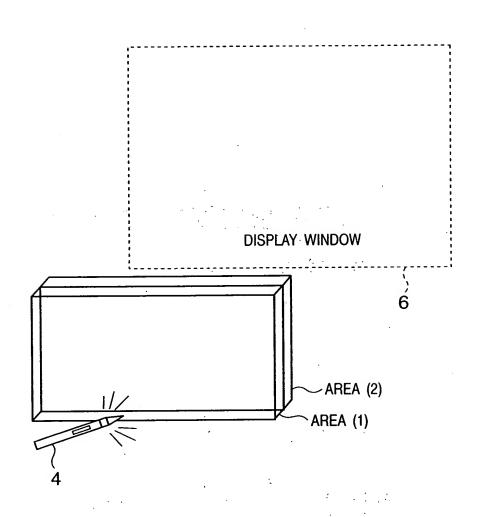
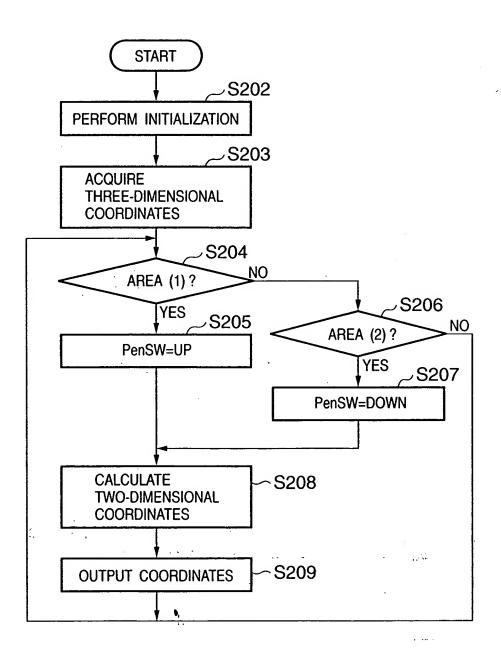
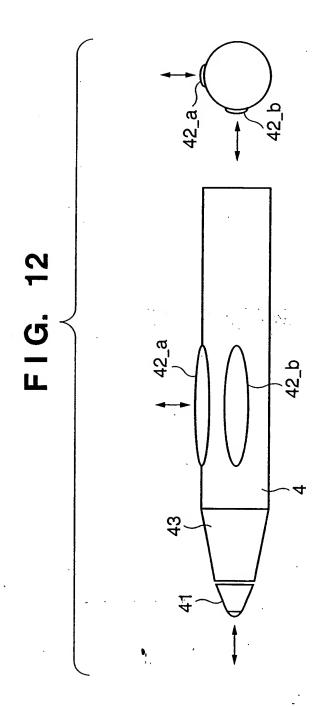


FIG. 10







AREA	PEN TIP SWITCH 41	PEN SIDE SWITCH 42_a	PEN SIDE SWITCH 42_b	PEN DOWN SIGNAL
AREA A	LEFT	LEFT	RIGHT	PEN TIP SW
AREA B	DOUBLE CLICK	RIGHT	LEFT	SIDE SW1
AREA C	LEFT	LEFT	RIGHT	DOWN